

## Proteus 2 Features

*Proteus 2 has a number of interfaces that can be combined as input/output mediums with Proteus Protocol stacks for a complete Communications System. This Technical Specification defines only one element of Proteus that should be read in conjunction with other interface specifications.*

## Proteus Analogue/POTS Interface

The Proteus processor can support up to 2 analogue/POTS interfaces. These interfaces have the 'look and feel' of standard PSTN telephone lines and can be used for setting up voice or audio calls over other Proteus interfaces such as ISDN, Ethernet and GPRS.

This interface can also be used for access to Proteus processor configuration parameters using simple '\*' commands.

The analogue interface includes tone detection and generation features and is designed to work with a low cost external SLIC circuit designed by Chiron. This means that no other major external chips are required and system costs are minimised.

## Feature Overview

Within a single Integrated Circuit package, Proteus includes all the major functions required for the ISDN interfaces:

- Separate audio in and out for '4 wire' interface
- On/off hook detection
- DTMF tone detection
- Local call progress tone generation as required (e.g. dial ring and busy tones)
- Ring signal generation with various cadences available
- Caller display signal generation using FSK
- Call progress indication using line reversal/line break
- LED drivers for port status indication

## Further Information Available

The following further information is available for Proteus with ISDN

- Technical specification (see web site [www.chiron.uk.com](http://www.chiron.uk.com))
- Reference Circuit Diagram
- Evaluation Board

